# **Frequency Synthesizer**

DSN-2300A-1119+

50Ω 1690 to 2310 MHz

# The Big Deal

- · Low phase noise and spurious
- Robust design and construction



CASE STYLE: KL942

## **Product Overview**

The DSN-2300A-1119+ is a Frequency Synthesizer, designed to operate from 1690 to 2310 MHz for Point-to-Point application. The DSN-2300A-1119+ is packaged in a metal case (size of  $1.25" \times 1.00" \times 0.20"$ ) to shield against unwanted signals and noise.

# **Key Features**

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -94 dBc/Hz typ. @ 10 kHz offset • Comparison Spurious: -94 dBc typ. • Reference Spurious: -115 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of DSN-2300A-1119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.







# Frequency Synthesizer

DSN-2300A-1119+

 $50\Omega$  1690 to 2310 MHz

#### **Features**

- Integrated VCO + PLL
- · Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+12.5V)



CASE STYLE: KL942 PRICE: \$41.95 ea. QTY (1-9)

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

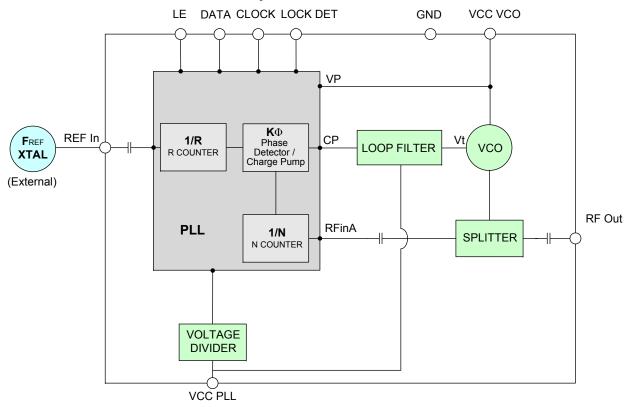
#### **Applications**

Point-to-Point

#### **General Description**

The DSN-2300A-1119+ is a Frequency Synthesizer, designed to operate from 1690 to 2310 MHz for Point-to-Point application. The DSN-2300A-1119+ is packaged in a metal case (size of 1.25" x 1.00" x 0.20") to shield against unwanted signals and noise. To enhance the robustness of DSN-2300A-1119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

#### **Simplified Schematic**



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Notes: 1. Performance and quality attributes and conditions not expressly stated in this specification sheet are intended to be excluded and do not form a part of this specification sheet. 2. Electrical specifications and performance data contained herein are based on Mini-Circuits applicable established test performance criteria and measurement instructions. 3. The parts covered by this specification sheet are subject to Mini-Circui standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this rat en entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp.

#### Electrical Specifications (over operating temperature -33°C to +80°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units	
Frequency Range		-	1690	-	2310	MHz	
Step Size		-	-	250	-	kHz	
Settling Time		Within ± 1 kHz	-	40	-	mSec	
Output Power		-	+2.5	+5.8	+8.5	dBm	
		@ 100 Hz offset	-	-65	-		
		@ 1 kHz offset	-	-71	-60	1	
SSB Phase Noise		@ 10 kHz offset	-	-94	-87	dBc/Hz	
		@ 100 kHz offset	-	-117	-109	1	
		@ 1 MHz offset	-	-138	-132	]	
Reference Spurious Suppress	sion	Ref. Freq. 10 MHz	-	-115	-90		
Comparison Spurious Suppre	ssion	Step Size 250 kHz	-	-94	-70	dD.	
Non - Harmonic Spurious Sup	pression	-	-	-90	-	dBc	
Harmonic Suppression		-	-	-30	-15	1	
VCO Supply Voltage		+5.00	+4.75	+5.00	+5.25	V	
PLL Supply Voltage		+12.50	+12.25	+12.50	+12.75	] V	
VCO Supply Current		-	-	34	42	mA	
PLL Supply Current		-	-	14	21	1 MA	
Reference Input	Frequency	10 (square wave) ensure slew rate (SR) > 50 V/μs	-	10	-	MHz	
(External)	Amplitude	1 - 1		-	V <sub>p-P</sub>		
(External)	Input impedance	-	- 100		-	ΚΩ	
	Phase Noise @ 1 kHz offset	-	-	-140	-	dBc/Hz	
RF Output port Impedance	,	-	-	50	-	Ω	
lament Lamia Lavral	Input high voltage	-	2.65	-	-	V	
Input Logic Level	Input low voltage	-	-	-	0.65	V	
Digital Lock Detect	Locked	-	2.85	-	3.70	V	
Digital Lock Detect	Unlocked	-	-	-	0.40	V	
Frequency Synthesizer PLL		-	ADF4106				
PLL Programming		-	3-wire serial 3.3V CMOS				
	F_Register	-	(MSB) 1001111111000000000010010 (LSB)			(LSB)	
Register Map @ 2310 MHz	N_Register	-	(MSB) 1000010010000001100001 (LSB)				
	R_Register	-	(MSB) 10000000000010100000 (LSB)			SB)	

## **Absolute Maximum Ratings**

Parameters	Ratings
VCO Supply Voltage	5.8V
PLL Supply Voltage	14.0V
VCO Supply Voltage to PLL Supply Voltage	N.A.
Reference Frequency Voltage	0Vmin, +3.55Vmax
Data, Clock, LE Levels	0Vmin, +3.55Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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### Typical Performance Data

FREQUENCY	POWER OUTPUT			VC	VCO CURRENT			PLL CURENT		
(MHz)		(dBm)			(mA)			(mA)		
	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C	
1690	5.42	5.19	4.57	32.68	34.68	35.98	11.89	13.51	15.41	
1728	5.58	5.39	4.68	32.32	34.45	35.84	11.88	13.52	15.41	
1796	5.66	5.19	4.35	32.11	34.18	35.66	11.91	13.55	15.45	
1864	5.39	5.04	4.13	31.80	33.97	35.56	11.90	13.55	15.44	
1932	5.65	5.39	4.46	31.60	33.85	35.50	11.93	13.57	15.47	
2000	5.64	5.38	4.47	31.50	33.80	35.51	11.93	13.57	15.47	
2068	5.61	5.54	4.66	31.46	33.80	35.53	11.95	13.59	15.49	
2136	6.10	6.15	5.35	31.50	33.87	35.59	11.94	13.58	15.48	
2204	6.39	6.49	5.80	31.66	34.03	35.71	11.96	13.60	15.50	
2272	6.33	6.62	6.11	31.86	34.24	35.88	11.95	13.59	15.48	
2310	7.00	7.18	6.71	31.98	34.37	36.01	11.97	13.62	15.50	

FREQUENCY	HARMONICS (dBc)							
(MHz)		F2			F3			
	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C		
1690	-22.55	-25.45	-30.57	-31.84	-37.17	-43.17		
1728	-23.09	-26.47	-32.94	-34.79	-41.39	-46.25		
1796	-26.84	-31.21	-39.16	-38.61	-42.85	-46.20		
1864	-29.49	-34.36	-42.57	-34.68	-37.45	-40.52		
1932	-31.62	-36.67	-45.96	-37.68	-38.51	-41.79		
2000	-35.04	-38.75	-46.61	-40.73	-41.25	-45.73		
2068	-36.18	-40.33	-48.28	-49.01	-50.91	-56.36		
2136	-35.23	-41.83	-59.58	-51.59	-54.33	-59.01		
2204	-36.51	-42.76	-52.34	-55.82	-56.47	-58.87		
2272	-34.67	-37.96	-45.01	-50.72	-51.46	-51.34		
2310	-37.25	-45.07	-45.82	-53.91	-55.39	-50.56		



FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS							
(MHz)	+25°C							
	100Hz	1kHz	10kHz	100kHz	1MHz			
1690	-67.91	-73.77	-94.96	-116.70	-137.04			
1728	-68.53	-73.97	-95.15	-116.90	-137.12			
1796	-67.97	-73.01	-95.87	-117.56	-137.56			
1864	-66.96	-72.66	-95.34	-117.55	-137.49			
1932	-66.59	-71.84	-94.85	-117.70	-137.85			
2000	-65.85	-70.56	-94.61	-117.76	-138.20			
2068	-65.18	-68.71	-94.05	-117.36	-138.23			
2136	-61.39	-67.90	-93.83	-117.08	-138.55			
2204	-60.10	-68.46	-93.20	-116.58	-138.79			
2272	-60.17	-66.14	-92.55	-115.35	-138.54			
2310	-59.10	-66.48	-91.74	-114.46	-138.15			

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS							
(MHz)								
, ,	100Hz	1kHz	10kHz	100kHz	1MHz			
1690	-68.72	-74.16	-96.36	-117.61	-137.35			
1728	-66.24	-74.77	-95.61	-117.11	-136.71			
1796	-67.62	-74.02	-95.13	-117.23	-136.98			
1864	-66.97	-74.72	-95.54	-117.50	-137.30			
1932	-65.64	-73.68	-96.05	-118.25	-138.20			
2000	-65.66	-72.60	-96.17	-118.97	-138.83			
2068	-67.19	-71.67	-95.97	-119.23	-139.13			
2136	-60.57	-71.28	-95.81	-119.38	-139.62			
2204	-62.29	-70.96	-95.41	-119.42	-140.19			
2272	-59.90	-67.33	-94.71	-118.77	-140.19			
2310	-60.76	-67.51	-94.30	-118.22	-140.34			

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)			+85°C						
	100Hz	1kHz	10kHz	100kHz	1MHz				
1690	-66.10	-72.18	-92.80	-114.50	-135.54				
1728	-67.51	-71.95	-93.02	-114.88	-135.65				
1796	-68.75	-72.68	-93.07	-115.16	-135.80				
1864	-69.71	-72.17	-92.69	-114.92	-135.55				
1932	-68.08	-71.52	-92.47	-114.74	-135.57				
2000	-67.09	-69.76	-92.32	-114.81	-135.79				
2068	-66.31	-69.29	-92.25	-114.82	-136.02				
2136	-63.15	-67.80	-91.97	-114.60	-136.16				
2204	-61.25	-67.27	-91.75	-114.41	-136.62				
2272	-59.32	-66.37	-91.14	-113.60	-136.51				
2310	-56.93	-63.23	-90.26	-112.74	-136.25				



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COMPARISON SPURIOUS ORDER		ARISON SPU @Fcarrier  z+(n*Fcom  (dBc) no	parison)	COMPARISON SPURIOUS  @ Fcarrier  2000MHz+(n*Fcomparison)  (dBc) note 1		parison)	COMPARISON SPURIOUS  @Fcarrier 2310MHz+(n*Fcomparison) (dBc) note 1		
n	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C
-5	-117.51	-118.76	-114.69	-117.07	-118.73	-115.11	-124.79	-119.65	-114.02
-4	-113.75	-119.93	-115.31	-114.95	-116.67	-112.64	-121.15	-118.68	-111.42
-3	-111.70	-115.77	-108.73	-110.18	-113.37	-107.98	-115.14	-114.08	-106.30
-2	-106.60	-109.36	-102.76	-105.33	-106.43	-102.71	-107.26	-110.82	-99.75
-1	-91.86	-97.17	-91.98	-93.99	-94.29	-90.84	-93.28	-101.15	-86.01
o <sup>note 2</sup>	-	-	-	-	-	-	-	-	-
+1	-91.16	-98.27	-91.51	-94.02	-93.93	-91.93	-92.69	-102.02	-86.55
+2	-104.36	-108.97	-102.93	-103.83	-106.78	-104.24	-105.54	-110.50	-99.68
+3	-111.28	-114.72	-108.18	-110.61	-114.41	-108.58	-115.87	-112.74	-106.80
+4	-113.75	-117.98	-114.14	-114.10	-118.99	-113.04	-121.95	-116.71	-112.77
+5	-114.72	-120.47	-119.43	-117.61	-124.23	-116.11	-126.22	-120.52	-116.52

Note 1: Comparison frequency 250 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER		RENCE SPU @Fcarrier Hz+(n*Frefe (dBc) no	erence)	REFERENCE SPURIOUS  @Fcarrier  2000MHz+(n*Freference)  (dBc) note 3		REFERENCE SPURIOUS  @ Fcarrier  2310MHz+(n*Freference)  (dBc) note 3			
n	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C	-38°C	+25°C	+85°C
-5	-133.80	-129.02	-129.64	-133.29	-129.47	-131.52	-128.33	-133.29	-134.79
-4	-132.95	-132.74	-129.95	-130.25	-130.93	-127.44	-135.33	-133.32	-134.89
-3	-123.37	-121.31	-119.50	-124.26	-119.21	-123.08	-122.32	-124.97	-123.39
-2	-133.02	-129.24	-128.24	-131.95	-129.92	-125.45	-129.19	-128.59	-131.04
-1	-114.90	-115.46	-113.37	-115.86	-114.51	-115.37	-118.09	-117.80	-120.18
o <sup>note 4</sup>	-	-	-	-	-	-	-	-	-
+1	-119.00	-118.28	-115.94	-118.78	-115.72	-117.18	-118.10	-119.46	-121.45
+2	-133.38	-129.14	-127.66	-133.85	-132.80	-128.32	-133.30	-129.83	-129.13
+3	-123.41	-129.01	-125.22	-122.61	-120.82	-119.94	-126.13	-122.83	-122.09
+4	-130.93	-134.55	-132.26	-133.55	-132.36	-131.69	-135.35	-132.37	-133.77
+5	-129.85	-133.62	-132.99	-133.88	-129.03	-130.27	-134.07	-134.91	-133.31

Note 3: Reference frequency 10 MHz

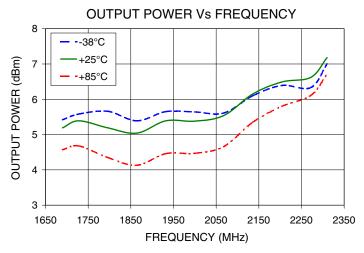
Note 4: All spurs are referenced to carrier signal (n=0).

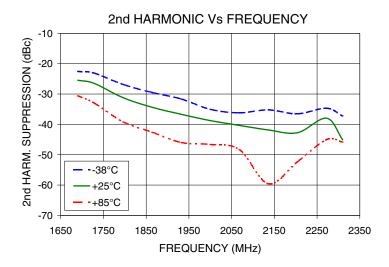


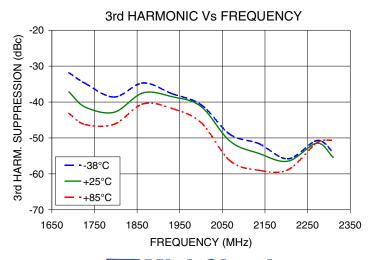
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#### Typical Performance Curves





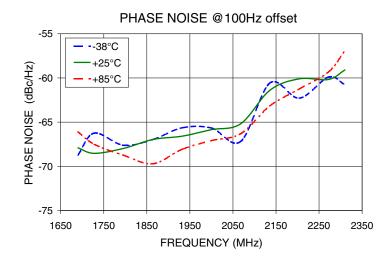


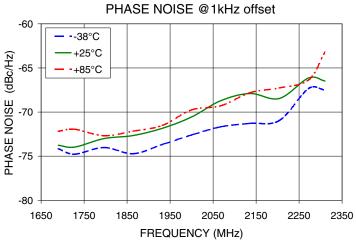
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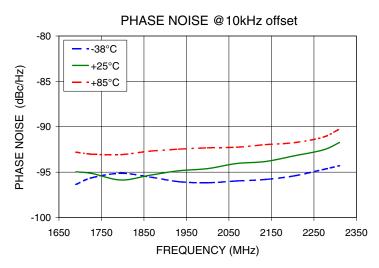
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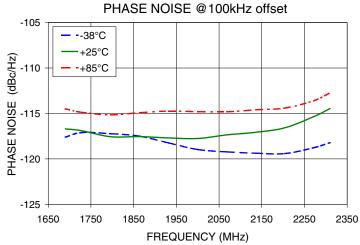
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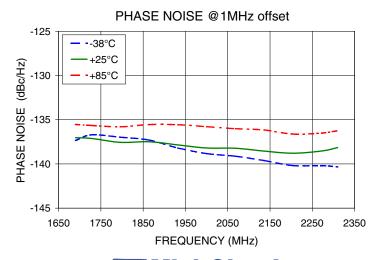










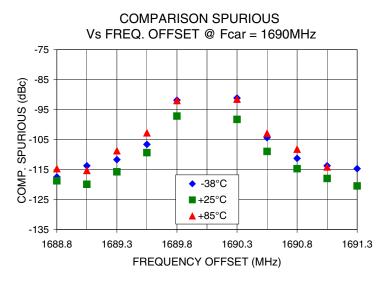


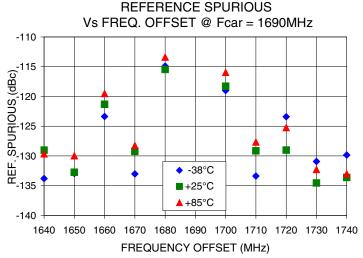
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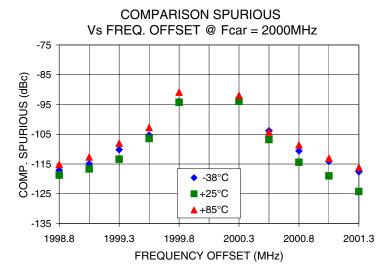
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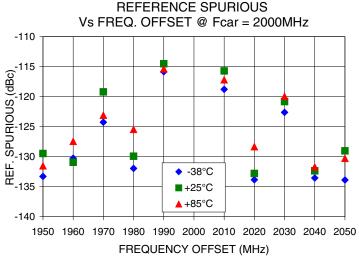
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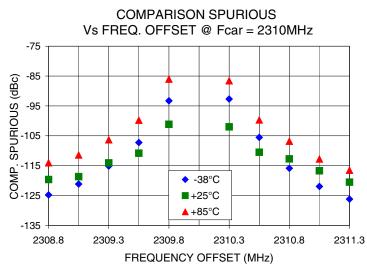
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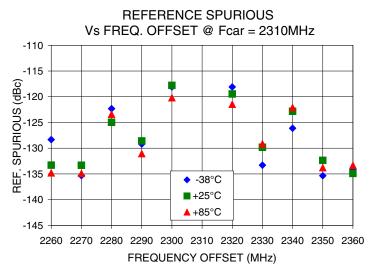












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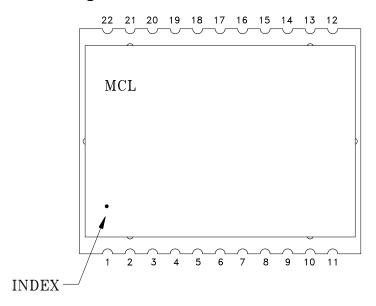
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### **Pin Configuration**

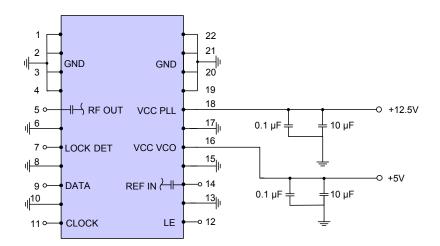


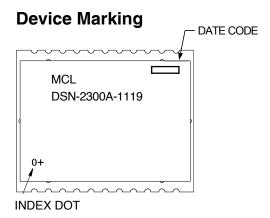
#### **Pin Connection**

Pin Number	Function	Pin Number	Function
1	GND	12	LE
2	GND	13	GND
3	GND	14	REF IN
4	GND	15	GND
5	RF OUT	16	VCC VCO
6	GND	17	GND
7	LOCK DET	18	VCC PLL
8	GND	19	GND
9	DATA	20	GND
10	GND	21	GND
11	CLOCK	22	GND

#### **Recommended Application Circuit**

Note: REF IN and RF OUT ports are internally AC coupled.





#### **Additional Detailed Technical Information**

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: KL942

Tape & Reel: TR-F97

Suggested Layout for PCB Design: PL-318

**Evaluation Board: TB-553+** 

**Environment Ratings:** ENV03T2

