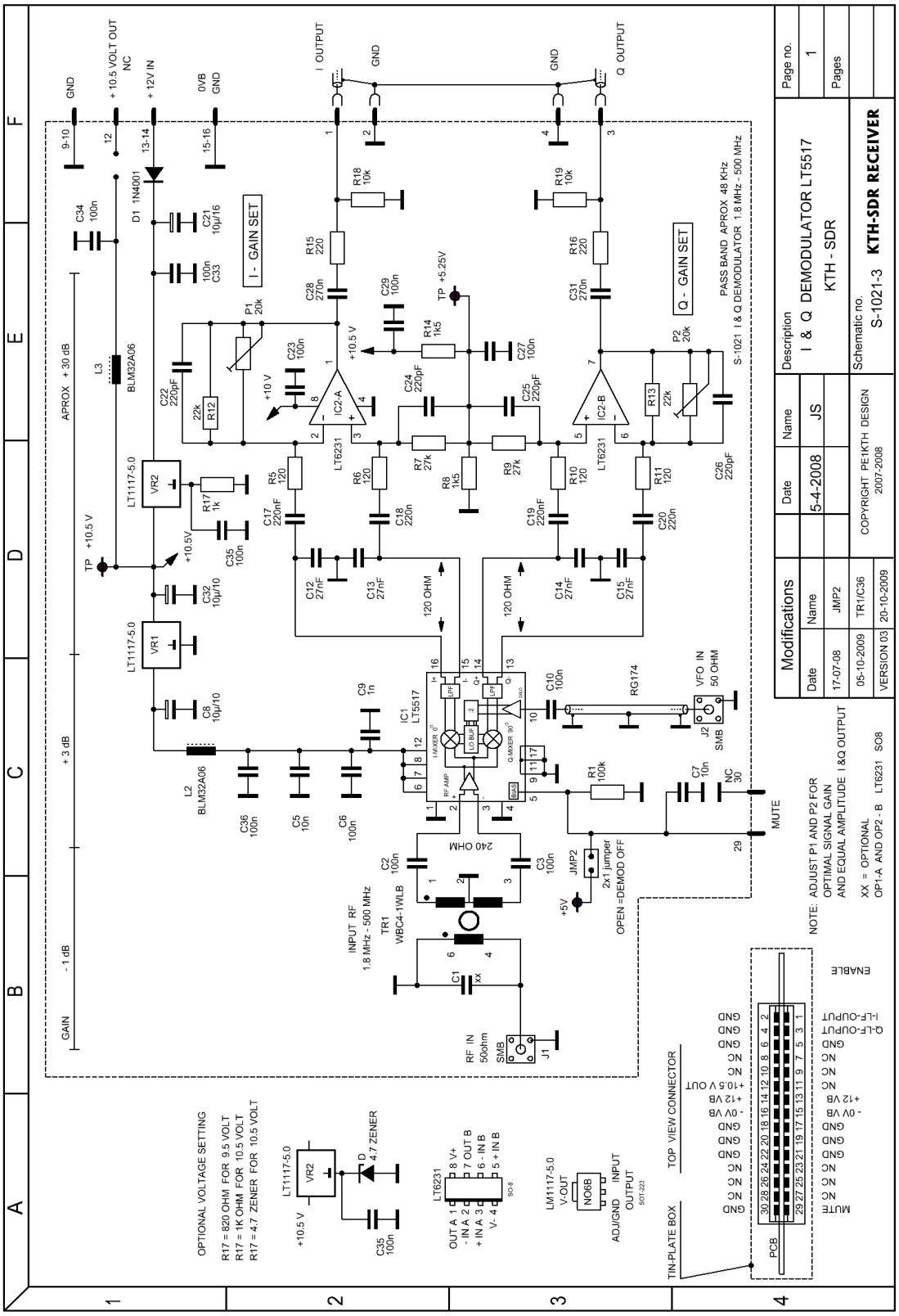

Figure 2.5-2 I & Q demodulator schematic.