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Features

- I CMOS Integrated Ku band to L band downconverter
- I Integrated VCO and PLL
- I Integrated pre-amplifier, mixer and L band buffer amplifier
- I Integrated reference oscillator (27MHz is default)
- I Integrated 22K tone detection and polar voltage selection
- I Integrated negative voltage generator and phemt

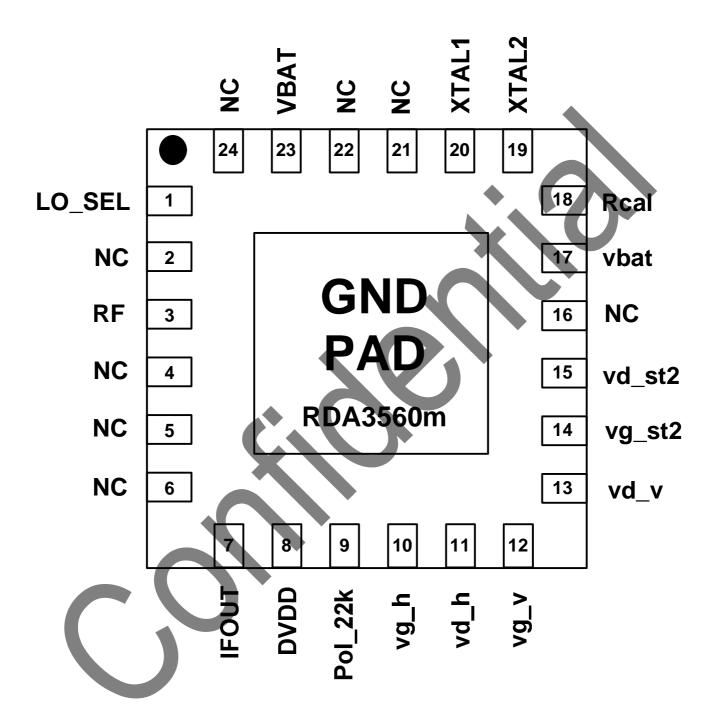
FET bias control

- I Low phase noise
- I Switched LO frequency between 9.75GHz and 10.6GHz
- I Also support 10.75GHz LO frequency
- I 4.5V to 5.5V operation
- I Low spurious
- Lower profile packages: 4×4 mm QFN24

Applications

- I Ku band LNB converters for digital satellite reception
- I Ku band LNB for Domestic ABS-S application

Pin Information



Pin Description

| Pin | Name | 1/0 | DESCRIPTION |
|--------|--------|-----|--|
| number | | | |
| 1 | LO_SEL | I | LO frequency seletion port. When connected to Vbat, 10.75GHz LO enabled, |
| | | | connected to gnd LO switched between 9.75GHz and 10.6GHz |

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ESD Sensitivity

Integrated circuits are ESD sensitive and can be damaged by static electricity. Proper ESD techniques should be used when handling these devices.

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