

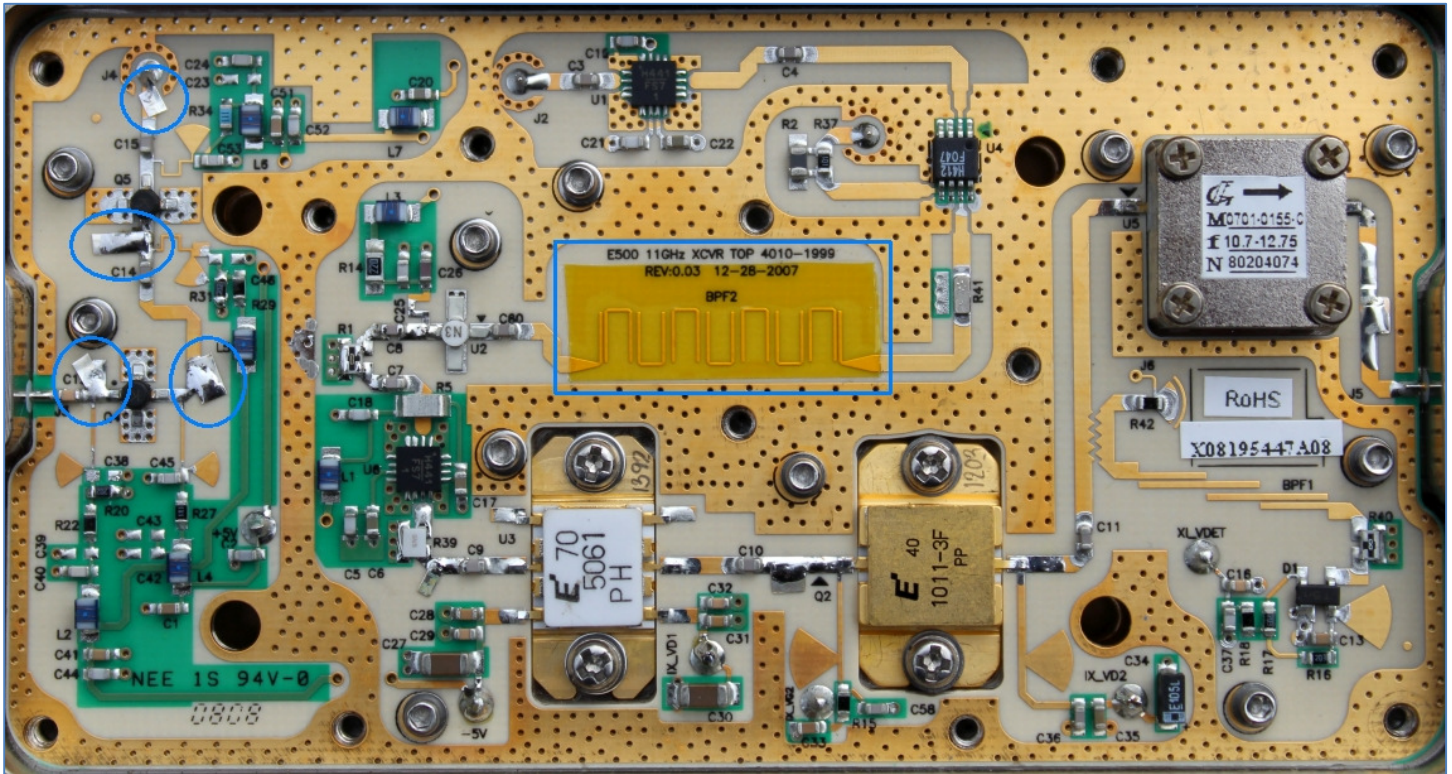


**During testing is mandatory to use 10V power supply with over current protection. Keep negative voltage -5V and PA bias connected all the time. Do not supply the PA stage without negative bias already applied!**

Clean the PCB after every modification. It is very important to avoid additional losses and wrong tuning due to residual flux.

TX IF level required 0...+3dBm. External TX/RX IF frequency is 1592MHz filtered by GPS SAW filters.

RX frontend view and TX BPF, FETs NE3210 (K) version:



#### MTI PCB BOTTOM SIDE modification:|

- Original LO BPFs have cutoff frequency at 6.3GHz and 12.7GHz. Multiplied LO signal 5980MHz and 11960MHz will be attenuated. LO Hairpins BPF1 & BPF2 retuned using KAPTON 0.05mm, 2 layers. Add KAPTON layers for best LO level (11960MHz) at the output of TX LO BPF3 – 3 layers KAPTON. Remove R74 attenuator (bridge) and connect bolometer sensor or spectrum analyzer by soldering a thin coaxial cable.
- Adjust LO BPF 1, 2, 3 using KAPTON layers until maximum RF LO level is reached (+3...5dBm on R74 test point), otherwise LO level is very low, around -8dBm and RX/TX performances are affected. The level is not critical due to HMC441 LO amplifiers in front of mixers.
- Stubs of LO x2 multipliers (U14 and U2 FET) tuned for maximum output level 11960MHz.
- No tune required for RX BPF, confirmed by NF test and Rx gain measurement. Any KAPTON layer decrease RX performance.
- The lid can be left open during tuning.







#### PLL Board modification:

- Original VCO module no need retuning. Output 2990MHz is inside of PLL lock range. Install 100 ohms resistor in parallel with R1 or R5 in order to increase VCO voltage (due to 10V power supply instead of 12V).
  - Remove HY1 TCXO and inject external 10MHz on TP13 or connect thin coaxial cable to output pad of HY1. Drill a hole 0.8mm in the center of TCXO output pad and then connect thin coaxial on the opposite side of PCB. Of course ground plane connected to coaxial shield.
- Good quality 10MHz OCXO is required, it will be used also for FI 2<sup>nd</sup> PLL LO 1592MHz / 144 (432) MHz board.

