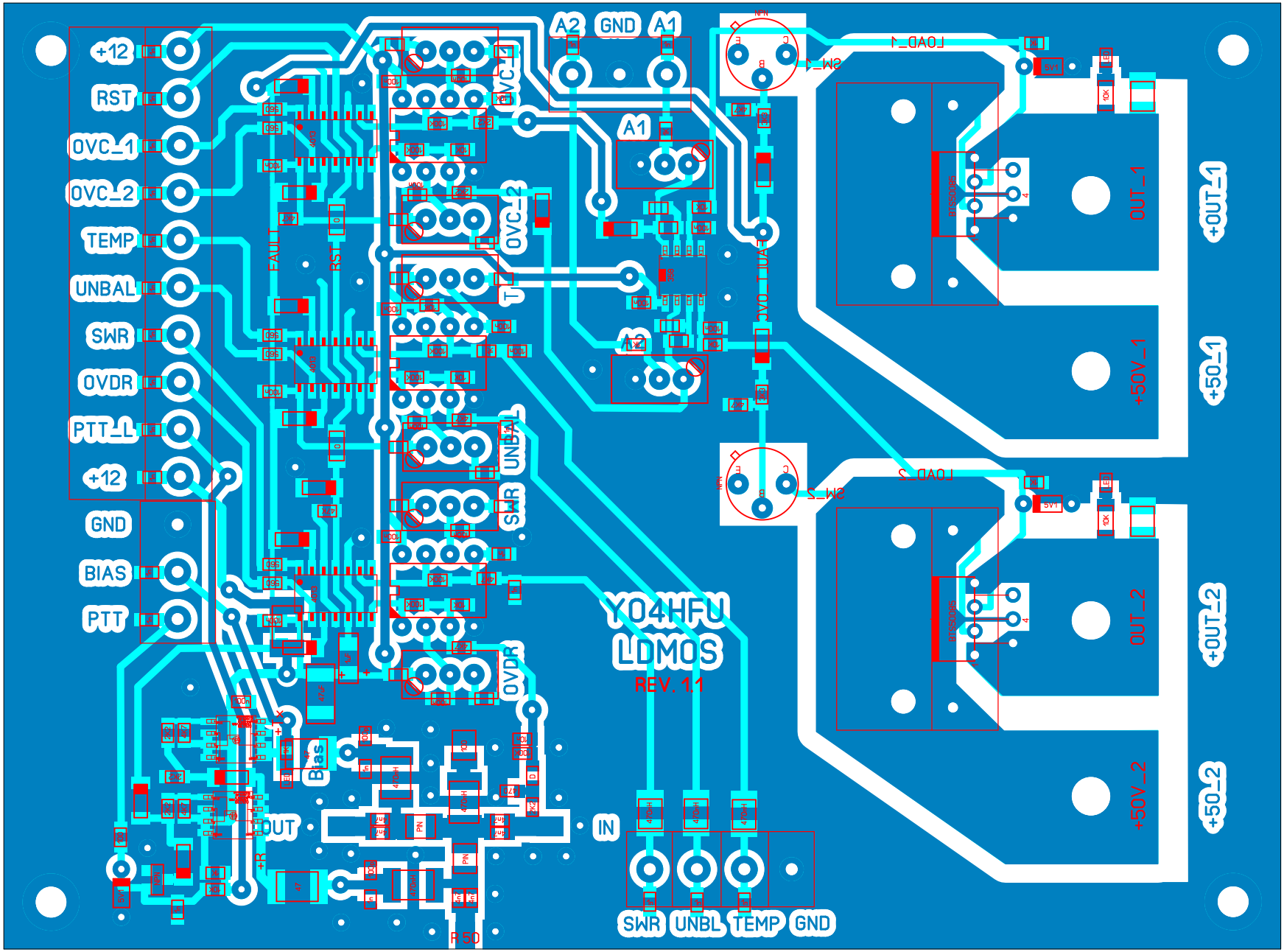
+12
RST
OVC_1
OVC_2
TEMP
UNBAL
SWR
OVDR
PTT_L
+12
GND
BIAS
PTT
A2
GND
A1
A1
A2
OVC_1
OVC_2
UNBAL
SWR
OVDR
Y04HFU
LDMOS
REV. 1.1
OUT
IN
Bias
R50
SWR
UNBL
TEMP
GND
OUT_1
OUT_2
+OUT_1
+OUT_2
+50V_1
+50V_2
+50_1
+50_2