

GB -  
GOOD SUPPLIER

LOW - NON - LINEARITY  
FREQUENCY / IN CORE SATURATES  
HIGH LOSSES  
LESS 80°C RISE

INPUT & OUTPUT DIFF  
 $\Delta V_{small}$  - TAKES  
TO RAMP CURRENT  
→ ~~500ns~~  
TAKES LONGER TO

8

6

5

POWER SUPPLY

BATTERY TEMP SENSE

→ SHOULD RUN CW

CHANGE  
1  $\mu F$  \* SKIN EFFECT PR  
AXIAL  
TO CROSS SECTION  
AREA OF WIRE

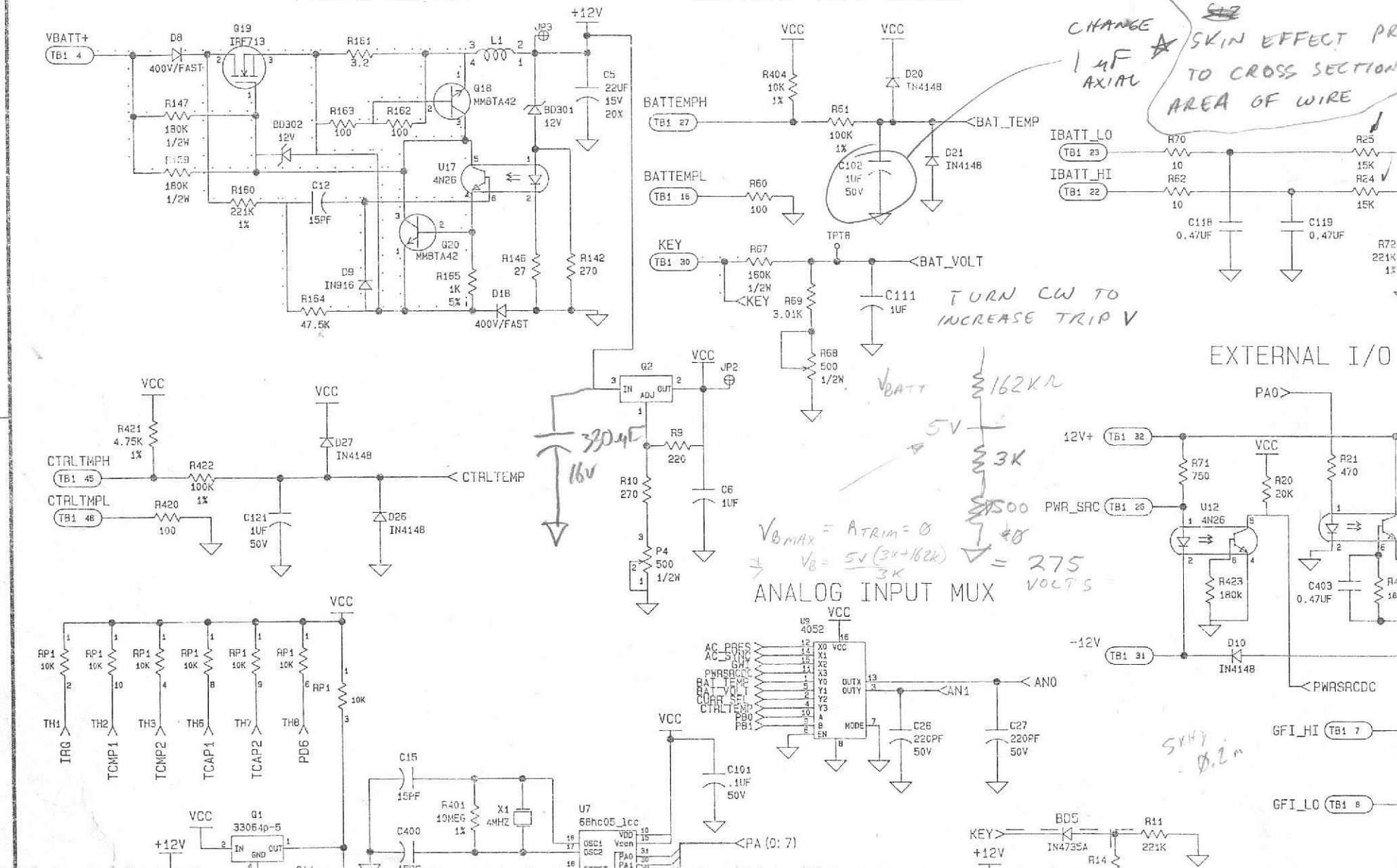
TURN CW TO  
INCREASE TRIP V

EXTERNAL I/O

$V_{B,MAX} = A \cdot TRIM = 0$   
 $V_B = \frac{5V(3K+162K)}{3K} = 275$   
VOLTS

ANALOG INPUT MUX

5x47  
0.2m



ONGER

P4 & R68  
SHOULD BE  
MULTI-TURN

BILL  
PETERSON  
607-770-3387  
α 8-255 350 VDC

LATEST AS OF JUN 1 1965  
- 125ms ON  
350-230  
= 120V  
 $L \frac{di}{dt} = V$   
 $t = L \frac{di}{V}$  15A  
RIPPLE

RAMP UP CURRENT

230 OUT →

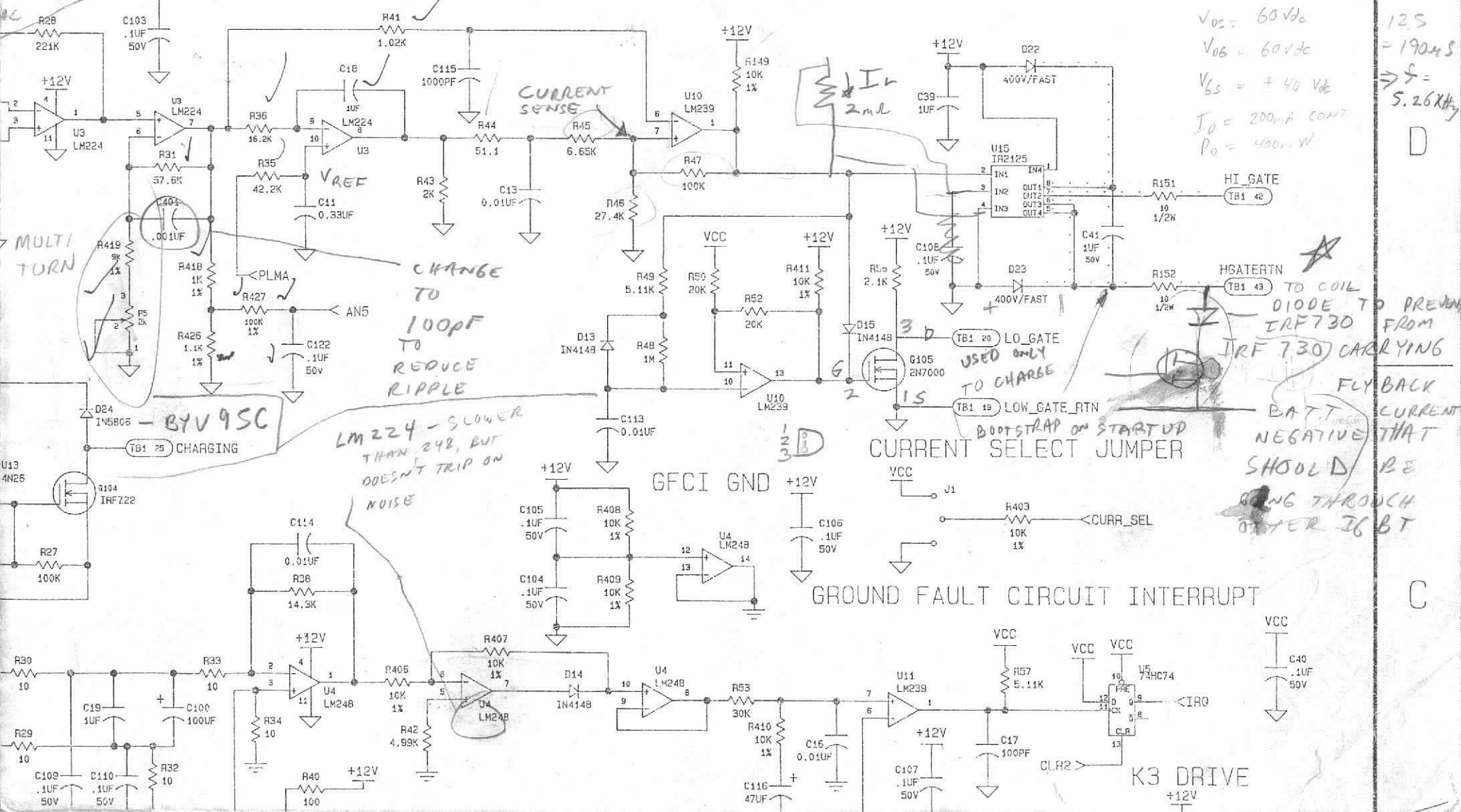
OLER (NEW ONE)

THESE ARE ALL STANDARD  
1% RESISTORS

DWG. NO.	SH	REV.	REVISIONS	
			ZONE	REV.
			DESCRIPTION	DATE
			APPROVED	

PROPORTIONAL

### HYSTERETIC CURRENT CONTROL



MULTI-TURN

LM224 - SLOWER THAN 242, BUT DOESN'T TRIP ON NOISE

CHANGE TO 100PF TO REDUCE RIPPLE

GFCI GND

CURRENT SELECT JUMPER

GROUND FAULT CIRCUIT INTERRUPT

K3 DRIVE

TO COIL DIODE TO PREVENT IRF730 FROM CARRYING FLYBACK BATT CURRENT NEGATIVE THAT SHOULD BE GOING THROUGH OTHER 36 BT

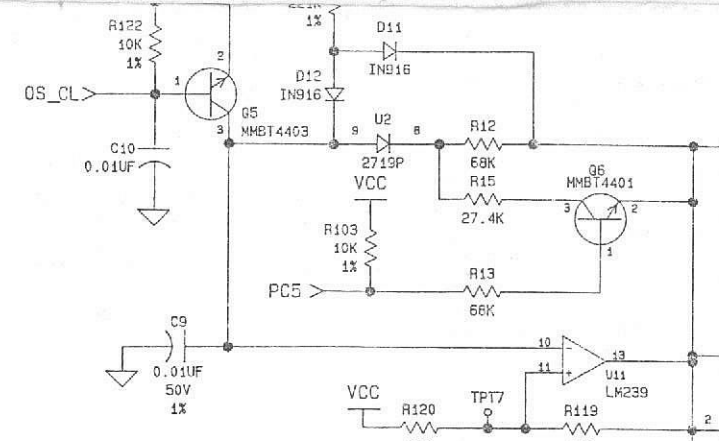
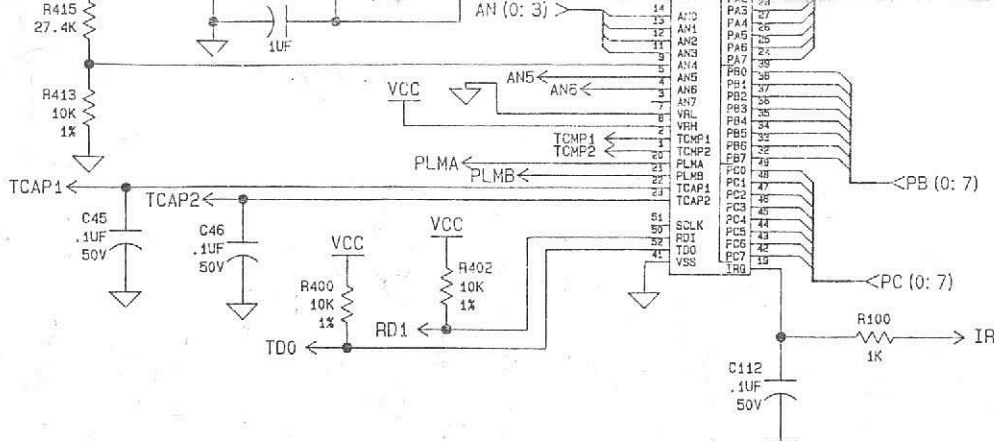
$V_{os} = 60V_{dc}$   
 $V_{ob} = 60V_{dc}$   
 $V_{cs} = +40V_{dc}$   
 $I_o = 200mA_{CONT}$   
 $P_o = 400mW$

65+  
12.5  
= 190ms  
→ f = 5.26kHz

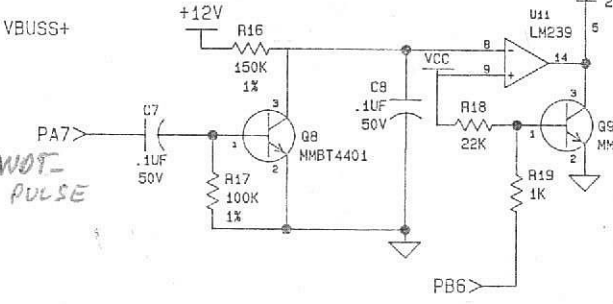
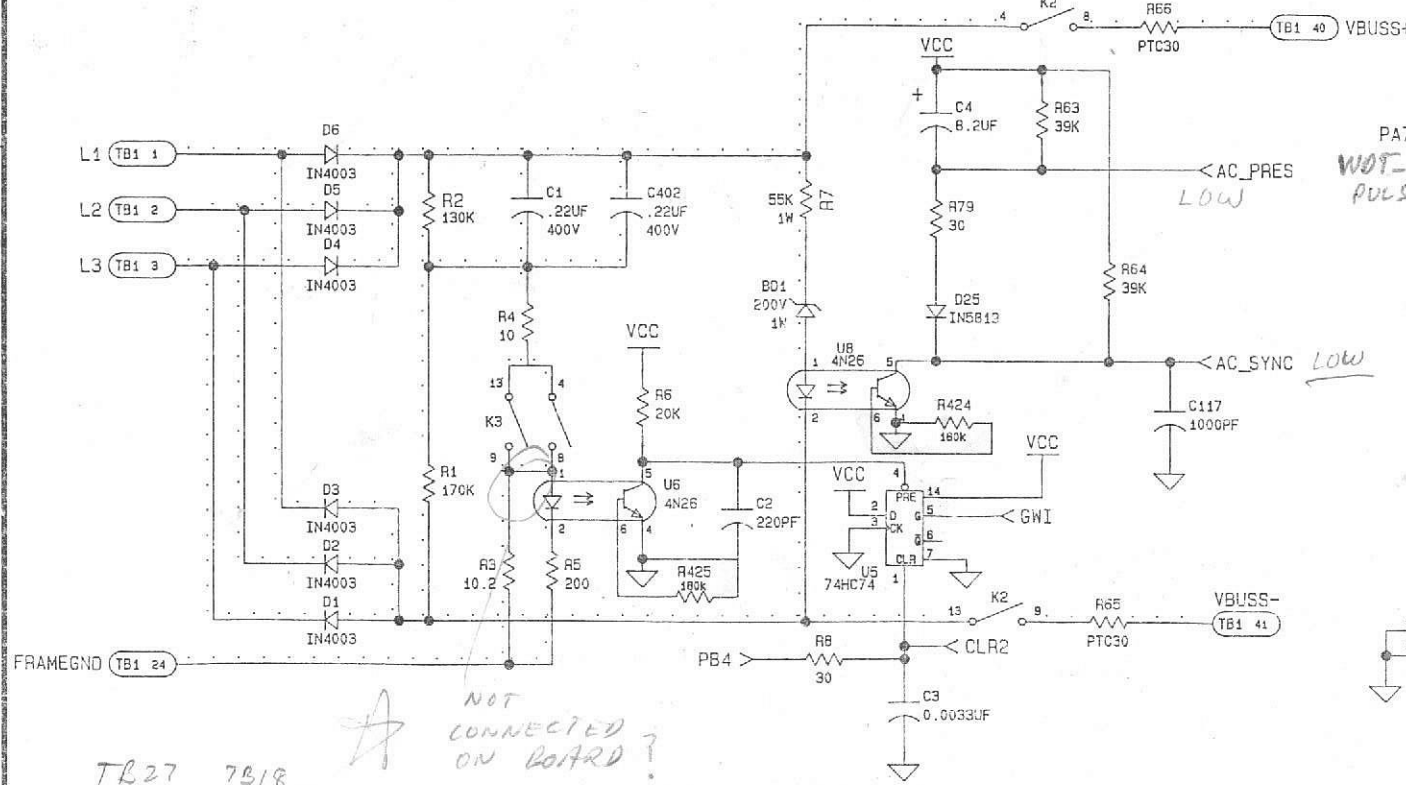
C

B

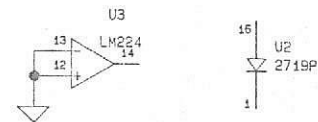
A



### GREEN WIRE INTEGRITY



#### SPARES:



#### NOTES:

- 1) Unless otherwise 5% tolerance.
- 2) All 'VCC' symbols
- 3) U6, 8, 12, 13, and
- 4) U2 is a FSA2719P
- 5) Last used refer
- 6) Unconnected / un
- 7) UL runs are mark

NOT CONNECTED ON BOARD?

TR27 7B18

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46

