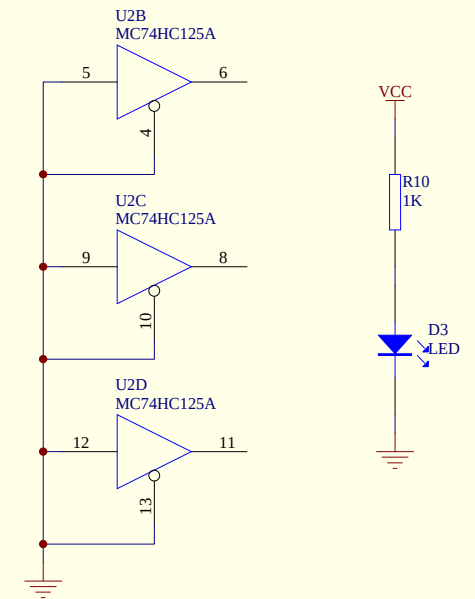
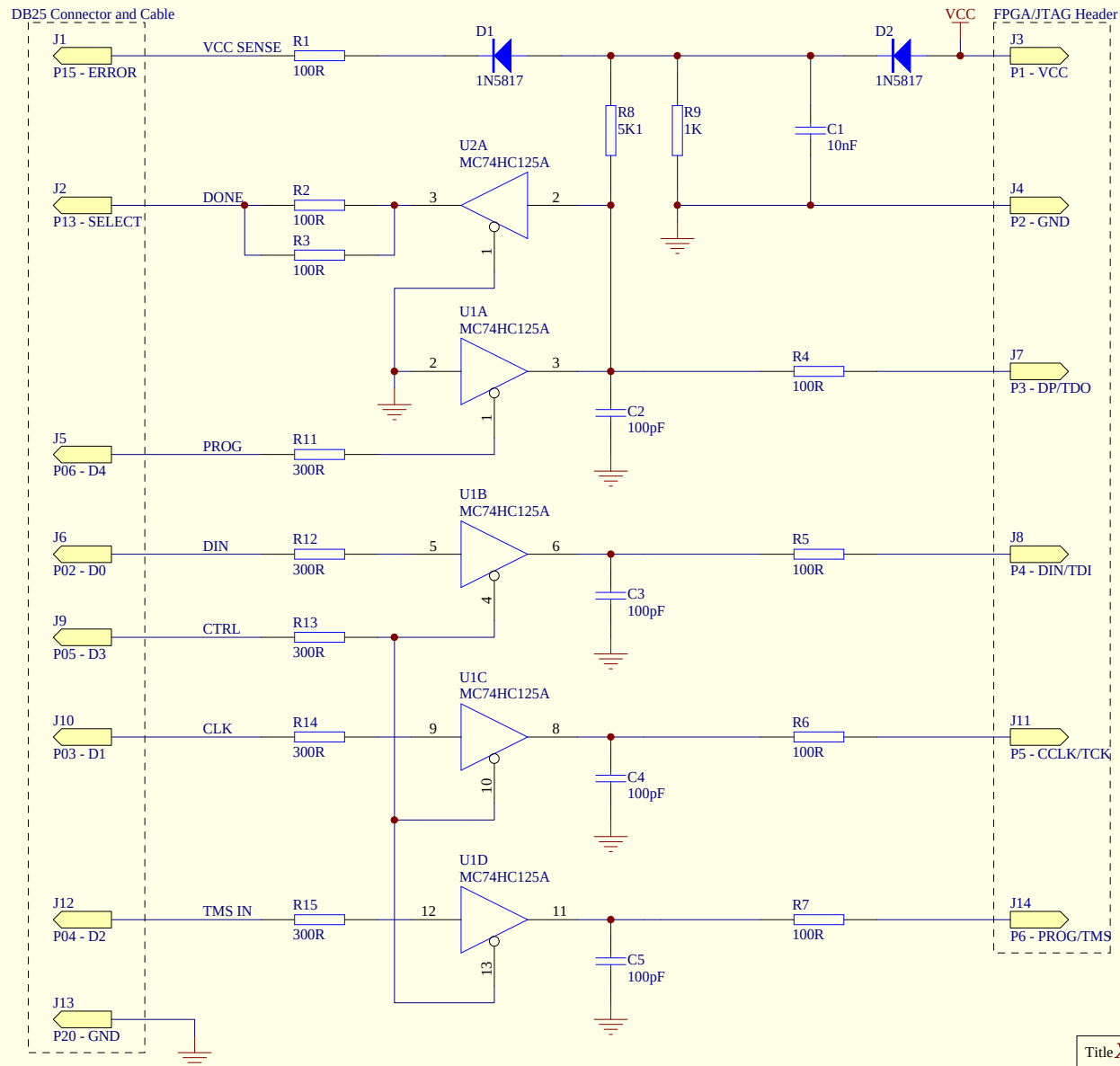




This schematic is courtesy of Xilinx Inc.

NOTE:
 1. Connect P8 (D6), P11 (BUSY) and P12 (PE) together at the end of data cable (DB25).
 2. Connect P20 (GND) and P25 (GND) together at the end of data cable.
 3. Connect shield of data cable to PC chassis/GND.

Title: Xilinx JTAG/Parallel Download Cable			Damjan Stritof s56wdf@qsl.net Cmri Potok 39 SI-1275 Smartno Slovenia
Size: A4	Number:	Revision: 0	
Date: 20-Nov-2001	Time: 23:21:01	Sheet 1 of 1	
File: D:\Projects\Xilinx-cable\Xilinx-cable.ddb - Documents\cable.sch			