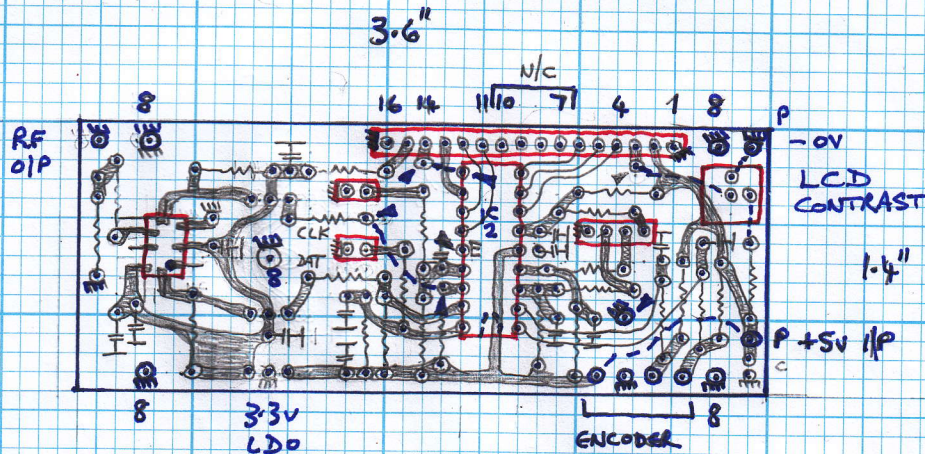


SB4E DIGITAL VFO PC BOARD V3 (FEB '21)



NOTES:

1. VIEWED FROM SOLDER (NON COMPONENT) SIDE
2. \blacktriangleright = THRO' LINK (EYGLLET)
3. $\#$ = GROUND (0V) CONNECTION
4. MATERIAL: $\frac{1}{16}$ " THK DOUBLE SIDED FIBREGLASS.
5. - - - - = NON ZERO VOLTS CONNECTION TRACK ON COMPONENT SIDE
6. \odot = PC PIN
7. \circ = 8BA (0.1") DIA MOUNTING HOLE
8. COMPONENT SIDE TRACK IS MOSTLY 0V (GROUND) TRACK
9. IC1 (51570) IS MOUNTED ON THE SOLDER SIDE
10. IC2 (16F88) IS MOUNTED ON A TURNED PIN IC SOCKET ON THE COMPONENT SIDE
11. THE LDO 3.3V REQUIRES A SMALL HEATSINK.
12. PC PINS POINT OUT OF THE SOLDER SIDE & LINK CONNECTORS