

GE R. Hogan W4XE
 11/20/2004 Master II IDA controller board

J1200

1	+10V	14	tx audio hi
2	+13.8V	15	rx1 mute*
3	GND	16	filtered rx audio (not-muted)
4	Vol/SqI hi	17	rprr ptt*
5	Delayed ptt*	18	RUS OS*
6	combined ptt*	19	Remote ptt*
7	rprr time out	20	local ptt*
8	cg mon* (channel guard)	21	over time signal gate
9	tone sw*	22	busy tone det (channel guard out)
10	tone ptt*	23	rf1 *
11	rprr audio mute	24	rf2*
12	key slot no connect	25	scan
13	ant relay*	26	RUS
*= active low signal			

J1201 FUNCTION

1	Filtered rx audio
2	rx1 mute *
3	ant relay*
4	TBD
5	channel guard mon
6	scan
7	rpt disable*
8	rf1 *
9	tf1/f1 *
10	tf2/f2*
11	cg tone det out
12	rf2*

*=active low

J1202

1	+10V ext
2	rprr ptt*
3	combined ptt*
4	local mic hi
5	local mic lo
6	TBD
7	local mic hi
8	local mic lo
9	icom audio
10	rx2 mute*
11	tone ptt*
12	2nd rx audio out

J1203

1	rem ptt*
2	spkr1 hi
3	+13.8 V
4	+10V ext
5	spkr1 hi
6	spkr1 lo
7	tx audio hi out (to exciter)
8	vol/sq hi
9	spkr1 lo
10	delayed ptt*
11	tx audio lo out (to exciter)
12	vol/sq lo

J1204

1	GND
2	over time signal gate
3	voting audio out
4	gnd
5	tx osc cntl
6	TBD
7	tx disable*
8	secure it tone*
9	TBD
10	regulated +10V
11	RUS
12	TBD

P1

	TBD

P2

1	+13.8 VDC
2	+10 VDC
3	GND
4	TBD
5	RUS IN

P3**LOCAL MIKE CONNECTOR**

1	Lcl mike lo
2	lcl mike hi
3	lcl ptt*
4	gnd

P4

1	Spkr 2 hi
2	spkr 2 lo

P5

	TBD
1	
2	
3	
4	
5	
6	
7	

P6

1	+10 VDC	9	tf2/f2 *
2	+10 VDC	10	?
3	+5 VDC	11	tf1/f1*
4	+5 VDC	12	SET (1950hz)
5	RESET FF	13	scan
6	GND	14	chn guard disable*
7	rptr disable *	15	rf2/f4*
8	TONE	16	rf1/f3*

P7

1	ant relay*
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TB1201

1	Filt rx audio
2	4 wire audio
3	line audio
4	line audio
5	4 wire audio
6	scan

TBD	To be determined
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