VFO in
5.05 -- 5.55MHz
a CR9
R14/GND
100p
~15cm
RG173
1n8
10k
1k
BC547
VFO input amplifier
74HC4060
Binairy counter
74HC4060
74HC74
Digital mixer
Charge pump
1N4148
10n
22k
BC557
4M7
BC547
1u
3k9
1k
+10
Reset
after reset
4.0V
CA3140
Integrator
47k
2u2
220k
100k
360k
100n
NE5534
Summing amplifier
To PTO RIT-in
0.4 -- 8.0V
brown
RIT to TR7 PTO
(remove wire from U1 pin 3,9)
39k
Ref in
40MHz
10/19
10/20
82p
BF494
220E
Reference input amplifier
wiper RIT cntrl
(U1 pin 11)
RIT in
1.6 -- 6.6V
12k
33k
2k7
LM258
Dual comparator
LED
out of range
560E
Rd
LED
Reset
10u
16V
680
Reset circuit
optional auto reset
2/14
2/15
red
black
+10V IN
2u2
25V
78L05
22u
10V
Huff Puff stabilizer for Drake TR7
Original design PA0KSB✞ 1996
Circuit diagram
PA0TRT 1-2004